



## **HPC Info Day**

Organised by ETP4HPC and the European Commission (DG CONNECT) on behalf of the Public-Private Partnership (cPPP) on HPC

with the local support of TERATEC

Wednesday, April 9<sup>th</sup>, 2014 - Paris

Venue: Institut de Physique du Globe - 1 Rue Jussieu, 75005 Paris

On December 17th, 2013, ETP4HPC, the European Technology Platform (ETP) in the area of High-Performance Computing (HPC) and the European Commission (EC) established a contractual Public-Private Partnership (PPP) for supporting the development of the European HPC eco-system and facilitate the achievement of research and investment objectives in the area of HPC technology provision, application development and sustainable HPC infrastructure. The HPC PPP is one of the eight contractual PPP's launched last December in the frame of the Horizon 2020 Programme for Research and Innovation (2014-2020).

This Info Day is the first public event within the charter of the PPP on HPC. The Programme includes the PPP's objectives, the European HPC strategy, and the Calls for Proposals related to HPC in the Work Programme 2014-2015 of the Excellent Science pillar of Horizon 2020 (Future and Emerging Technologies (FET) and e-infrastructures).

Participants will also have an opportunity to familiarise themselves with new ideas for ecosystem related actions, share their ideas, or just present themselves and their expertise.

The capacity of the venue is limited to 150 people, so please register as soon as you have confirmed your participation at the following address:

http://www.teratec.eu/Seminaires/inscription\_seminaire\_ETP4HPC.php

Please register by 4th April.





## Agenda

Registration		09:30-10:00
Welcome address	Jean-François Lavignon	10:00-10:05
	Chair of ETP4PHC	
HPC Strategy in H2020 & HPC cPPP	Thierry Van der Pyl	10:05-10:50
	Director of "Excellence in Science",	
	EC DG-CONNECT	
PRACE Contributions to the HPC Strategy	Dr. Thomas Eickermann	10:50-11:05
	PRACE Board of Directors	
ETP4HPC vision for HPC ecosystem	Jean-François Lavignon	11:05-11:35
development	Chair of ETP4HPC	
HPC Technologies: FETHPC call in Work	Panos Tsarchopoulos	11:35-12:15
Programme 2014-2015 + Q&A	FET, EC DG-CONNECT	
e-infrastructures: Centres of Excellence for	Aniyan Varghese	12:15-12:45
Computing Applications, PRACE and	e-infrastructure, EC DG-CONNECT	
Network of Competence Centres + Q&A		
Lunch & networking		12:45-13:45
Presentations by participants		13:45-15:45
(3 to 5 min slots – TBD)		
Wrap-up		15:45-16:00
Break & networking		16:00-16:30

Note that for the members of the ETP4HPC there will be a General Assembly after the Info Day:

ETP4HPC General Assembly	16:30-17:45
<ul> <li>ETP4HPC activities 2013</li> </ul>	
• ETP4HPC 2014 plan	
<ul> <li>ETP4HPC financial information and</li> </ul>	
vote	